

What is claimed is:

- 1           1.     A method comprising:  
2                 receiving a video frame;  
3                 identifying noise in a first portion of the video frame; and  
4                 replacing the first portion with a second portion of the video frame.
- 1           2.     The method of claim 1, wherein identifying further comprises:  
2                 associating a noise level with the first portion of the video frame;  
3     and  
4                 comparing the noise level to a predetermined value.
- 1           3.     The method of claim 2, wherein associating further  
2     comprises distinguishing the first portion from the second portion.
- 1           4.     The method of claim 3, wherein distinguishing further  
2     comprises:  
3                 associating a first value with the first portion;  
4                 associating a second value with the second portion; and  
5                 performing a plurality of arithmetic operations between the  
6     first value and the second value.
- 1           5.     The method of claim 4, wherein associating a first value with the  
2     first portion further comprises:  
3                 identifying a plurality of values associated with the first portion;  
4     and  
5                 performing an arithmetic operation on the plurality of values to  
6     render the first value.

1           6. The method of claim 2, wherein comparing the noise to a  
2 predetermined value comprises comparing the noise to a noise level found in a  
3 second video frame.

*B' could*  
1           7. The method of claim 2, wherein comparing the noise to a  
2 predetermined value comprises associating the predetermined value to the type  
3 of video input signal.

1           8. The method of claim 2, wherein comparing the noise to a  
2 predetermined value comprise associating the predetermined value to the type of  
3 noise in the video frame.

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*Sub A'*  
1           9. A system including:  
2 a bus;  
3 a processor coupled to the bus;  
4 a device coupled to the bus to receive a video signal; and  
5 a storage medium coupled to the bus including a software program  
6 that, upon execution:  
7 detects noise in a first portion of a video frame of the video  
8 signal; and  
9 replaces a first portion of the video frame.

*B' could*  
1           10. The system of claim 9, wherein the video frame is stored in a  
2 memory and, upon execution, the software program writes to the memory to  
3 replace the first portion of the video frame.

1 11. The system of claim 10, wherein, upon execution, the software  
2 program further detects noise by comparing a noise level associated with the  
3 first portion of the video frame with a predetermined value.

1 12. The system of claim 11, wherein the predetermined value is stored  
2 in the memory.

1 13. The system of claim 12, wherein the predetermined value is related  
2 to a noise level found in a second video frame.

1 14. The system of claim 10, wherein the predetermined value is  
2 related to the type of video signal.

1 15. The system of claim 9, wherein the storage medium is a hard disk  
2 drive.

1 16. An article comprising a medium storing instructions that cause a  
2 processor-based system to:  
3 locate a video frame of a video signal;  
4 identify noise in a first portion of the video frame; and  
5 replace the first portion with a second portion of the video frame.

1 17. The article of claim 16, further storing instructions that cause the  
2 processor-based system to locate the video frame by reading a memory device.

1 18. The article of claim 17, further storing instructions that cause the  
2 processor-based system to:  
3 associate a noise level with the first portion of the video frame; and

4 compare the noise level to a predetermined value.

1 19. The article of claim 18, further storing instructions that cause the  
2 processor-based system to:

3 associate a first value with the first portion;

4 associate a second value with the second portion; and

5 perform a plurality of arithmetic operations between the first value  
6 and the second value.

1 20. The article of claim 19, further storing instructions that cause the  
2 processor-based system to:

3 identify a plurality of values associated with the first portion; and

4 perform an arithmetic operation on the plurality of values to render  
5 the first value.

1 21. The article of claim 18, further storing instructions that cause the  
2 processor-based system to compare the noise level to a predetermined value by  
3 associating the predetermined value with a noise level found in a second video  
4 frame.

5 22. The article of claim 16, wherein the medium storing instructions is  
6 a memory device.

1 23. The article of claim 18, further storing instructions that cause the  
2 processor-based system to compare the noise level to a predetermined value by  
3 associating the predetermined value to the type of video signal.

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